	Search Text
1	mosaics near2 image
2	mosaics near2 image and "716"/\$.ccls.
3	mosaics near2 image and "716"/\$.ccls. and concurrent near3 engineer\$5
4	mosaics near2 image and concurrent near3 engineer\$5
5	mosaic\$4 near2 image and revers\$5 near3 engineer\$4
6	mosaic\$4 near2 image and revers\$5 near3 engineer\$4 and design\$4 same circuit
7	mosaic\$4 near2 image and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4
8	mosaic\$4 near2 image and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station)
9	mosaic\$4 near2 image and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018"
10	(mosaic\$4 near2 image or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018"
11	"20040117750"
12	(mosaic\$4 near2 (image photo) or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018"
13	(mosaic\$4 near3 (image photo view\$4 disaly\$4) or photomosaic\$4) and mosaic\$5 and revers\$5 near4 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018"
14	(mosaic\$4 near3 (image photo view\$4 disaly\$4) or photomosaic\$4) and mosaic\$56 same circuit and revers\$5 near4 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018"
15	(mosaic\$4 near2 (image photo) or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and ownership and annotat\$5
16	"20040117750" and (ownership)
17	(mosaic\$4 near2 image or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and ownership
18	(mosaic\$4 near2 image or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and ownership and (parallel concurrent\$5 simultan\$7) same (design circuit engineer\$4 reserv\$5)
19	(mosaic\$4 near3 (image photo view\$4 disaly\$4) or photomosaic\$4) and mosaic\$56 same circuit and revers\$5 near4 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and (parallel concurrent\$5 simultan\$7) same (design circuit engineer\$4 reserv\$5 project\$4)
20	(mosaic\$4 near3 (image photo view\$4 disaly\$4) or photomosaic\$4) and mosaic\$56 same circuit and revers\$5 near4 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and (parallel concurrent\$5 simultan\$7) same (design circuit engineer\$4 reserv\$5 project\$4 anal\$6)

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(mosaic\$4 near3 (image photo view\$4 disaly\$4) or photomosaic\$4) and mosaic\$56 same circuit and revers\$5 near4 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and (parallel concurrent\$5 simultan\$7) same (design circuit engineer\$4 reserv\$5 project\$4 anal\$6) and lock\$4 and annotat\$4
(mosaic\$4 near3 (image photo view\$4 disaly\$4) or photomosaic\$4) and mosaic\$56 same circuit and revers\$5 near4 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and (parallel concurrent\$5 simultan\$7) same (design circuit engineer\$4 reserv\$5 project\$4 anal\$6) and lock\$4
(mosaic\$4 near3 (image photo view\$4 disaly\$4) or photomosaic\$4) and mosaic\$56 same circuit and revers\$5 near4 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and (parallel concurrent\$5 simultan\$7) same (design circuit engineer\$4 reserv\$5 project\$4 anal\$6) and annotat\$4
revers\$6 same engineer\$4 same concurrent\$ same well adj known and @ad<"20001018"
revers\$6 same engineer\$4 same concurrent\$ same conventional and @ad<"20001018"
revers\$6 same (design) same circuit same (concurrent\$ silmutan\$6) same well adj know and @ad<"20001018"
revers\$6 same circuit same (concurrent\$ silmutan\$6) same well adj know and @ad<"20001018"
revers\$6 same engineer\$4 same extract\$4 same circuit
revers\$6 same engineer\$4 same extract\$4 same circuit and mosaics
revers\$6 same engineer\$4 same extract\$4 same circuit and mosaics and @ad<"20001018"
((plural\$5 multi\$6 many different) adj engineer\$5) same revers\$6 same engineer\$5 same well adj known and @ad<"20001018"
revers\$6 same engineer\$4 same extract\$4 same circuit same well adj known and @ad<"20001018"
revers\$6 same engineer\$4 same extract\$4 same circuit same common and @ad<"20001018"
revers\$6 same engineer\$4 adj2 analy\$6 same circuit and @ad<"20001018"
engineer\$5 same anal\$6 same concurrent\$4 same (circuit)
(plural\$5 multi\$5 many different) same engineer\$5 same anal\$6 same concurrent\$4 same (circuit)
(plural\$5 multi\$5 many different) same engineer\$5 same analy\$5 same (simultan\$6) same (circuit)
"20040117750" and multi\$5 same user same extension
"20040117750" and ownership near3 annotat\$5
lock\$4 same (edit\$4 modif\$6) and @ad<"20001018"
lock\$4 same (edit\$4 modif\$6) same engineer\$4 and @ad<"20001018"
lock\$4 same (edit\$4 modif\$6) same engineer\$4 adj analy\$5 and @ad<"20001018"
lock\$4 same (edit\$4 modif\$6) same engineer\$4 same (conflict contradict\$4) and @ad<"20001018"
lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4) same engineer\$4 same (conflict integrity contradict\$4) and @ad<"20001018"
lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4) same engineer\$4 same (conflict\$4 integrity

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46	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity contradict\$4) same engineer\$4 same (accesss\$4) and @ad<"20001018"
47	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity contradict\$4) same engineer\$4 same (access\$4) and @ad<"20001018"
48	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity contradict\$4) same engineer\$4 same (access\$4) and @ad<"20001018" and concurrent\$4 with engineer\$4 with analy\$5
49	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity contradict\$4) same engineer\$4 same (access\$4) and @ad<"20001018" and concurrent\$4 with engineer\$4
50	(block\$5) lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity contradict\$4) same engineer\$4 same (access\$4) and @ad<"20001018" and concurrent\$4 with engineer\$4
51	(block\$5 lock\$4) same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity contradict\$4) same engineer\$4 same (access\$4) and @ad<"20001018" and concurrent\$4 with engineer\$4
52	(lock\$4) same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity contradict\$4) same (engineer\$4 user) same (access\$4) same (accident\$5 inadv\$6) and @ad<"20001018" and concurrent\$4 with engineer\$4
53	((mosaic\$4 same (ic circuit layout\$5)).ab. or (mosaic\$4 same (ic circuit layout\$5)).bsum.) and @ad<"20001018"
54	((mosaic\$4 same (ic circuit layout\$5)).ab. or (mosaic\$4 same (ic circuit layout\$5)).bsum.) and @ad<"20001018" and (internet network remote lan wan)
55	((mosaic\$4 same (ic circuit layout\$5)).ab. or (mosaic\$4 same (ic circuit layout\$5)).bsum.) and @ad<"20001018" and (internet network remote lan wan) and (engineer analyst user) same (lock\$4 access\$4 authori\$6 identi\$6)
56	mosaic and concurrent with engineer\$\$
57	mosaic and concurrent with engineer\$6
58	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018"
59	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and (concurrent\$4 with engineer\$4)
60	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and revers\$5 near3 engineer\$4 and design\$4 same circuit and layout\$4 and (workstation work adj station) and @ad<"20001018" and (different multi\$4 concurrent\$5) with (user engineer\$4)
61	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and (revers\$5 near3 engineer\$4) same (ic chip circuit) and design\$4 same (circuit layout\$4) and (workstation work adj station) and @ad<"20001018" and (different multi\$4 concurrent\$5) with (user engineer\$4)
62	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and (revers\$5 near3 engineer\$4) same (ic chip circuit) and design\$4 same (circuit layout\$4) and (workstation work adj station) and @ad<"20001018" and (different interact\$6 multi\$4 concurrent\$5) with (user engineer\$4)
63	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and (revers\$5 near3 engineer\$4) same (ic chip circuit) and design\$4 same (circuit layout\$4) and (workstation work adj station) and @ad<"20001018" and (different interact\$6 many multi\$4 concurrent\$5) with (user engineer\$4)

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64	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and (revers\$5 near3 engineer\$4) same (ic chip circuit) and design\$4 same (circuit layout\$4) and (computer workstation work adj station) and @ad<"20001018" and (different interact\$6 many multi\$4 concurrent\$5) with (user engineer\$4)
65	(mosaic\$6 near3 (image photo view display\$5) or photomosaic\$4) and mosaic\$5 and (revers\$5 near3 engineer\$4) same (ic chip circuit) and design\$4 same (circuit layout\$4) and (computer workstation work adj station) same (lan wan network remote on adj line online) and @ad<"20001018" and (different interact\$6 many multi\$4 concurrent\$5) with (user engineer\$4)
66	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity access\$4 contradict\$4) and (user engineer) same (authori\$6 id ident\$6 access\$4) and @ad<"20001018"
67	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity access\$4 contradict\$4) and (user engineer) same (authori\$6 id ident\$6 access\$4) and @ad<"20001018" and (network wan lan remot\$4 server) same circuit same design\$4
68	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity access\$4 contradict\$4) and (user engineer) same (authori\$6 id ident\$6 access\$4) and @ad<"20001018" and (network wan lan remot\$4 server) same circuit same design\$4 and (well adj known) same concurrent\$ same engineer\$4
69	lock\$4 same (edit\$4 modif\$6 read\$4 writ\$4 conflict\$4 integrity access\$4 contradict\$4) and (user engineer) same (authori\$6 id ident\$6 access\$4) and @ad<"20001018" and (network wan lan remot\$4 server) same circuit same design\$4 and (well adj known) same concurrent\$ same (engineer\$4 design\$4) same circuit
70	(well adj known) same concurrent\$ same (engineer\$4 design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server)
71	(well adj known) same concurrent\$ same (engineer\$4 design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4
72	(well adj known) same concurrent\$ same (engineer\$4 design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$6)
73	(well adj known) same concurrent\$ same (engineer\$4 design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$5 author\$6 access\$4)
74	(well adj known) same (concurrent\$ plural\$4 simultan\$6) same (engineer\$4 design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$5 author\$6 access\$4)
75	(well adj known) same (concurrent\$ plural\$4 simultan\$6) same (engineer\$4 design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$5)
76	(well adj known) same (concurrent\$ plural\$4 simultan\$6) same (engineer\$4 user design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$5) and shar\$4 same (engineer\$4 user design\$4)
77	(well adj known) same (share) same (engineer\$4 user design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$5) and shar\$4 same (engineer\$4 user design\$4)
78	(well adj known conventional) same (share) same (engineer\$4 user design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$5) and shar\$4 same (engineer\$4 user design\$4)

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79	(well adj known conventional) same (shar\$5) same (engineer\$4 user design\$4) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and user same (id identi\$5) and shar\$4 same (engineer\$4 user design\$4)
80	(well adj known conventional\$4) same (concurrent\$ plural\$4 simultan\$6) same (engineer\$4 user designer) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and (engineer designer user) same (id identi\$5) ownership
81	(well adj known conventional\$4) same (concurrent\$ plural\$4 simultan\$6) same (engineer\$4 user designer) same circuit and @ad<"20001018" and (network wan lan remot\$4 server) and lock\$4 and (engineer designer user) same (id identi\$5) and ownership
82	(mosaic\$4 with (image picture photo view\$4 display\$4) or photomosaic\$4) same circuit and (revers\$5 with engineer\$4) same (circuit ic chip) and (workstation work adj station) and @ad<"20001018"
83	(mosaic\$4 with (image picture photo view\$4 display\$4) or photomosaic\$4) same circuit and (revers\$5 with engineer\$4) same (circuit ic chip) and (workstation work adj station) and @ad<"20001018" not skoll
84	(mosaic\$4 with (image picture photo view\$4 display\$4) or photomosaic\$4) same circuit and (revers\$5 with engineer\$4) same (circuit ic chip) and (computer) and @ad<"20001018"